


[Subscribe](#) (Full Service) [Register](#) (Limited Service, Free) [Login](#)

 Search: ☒ The ACM Digital Library ☐ The Guide



THE ACM DIGITAL LIBRARY


[Feedback](#) [Report a problem](#) [Satisfaction survey](#)

 Terms used **successive approximation**

 Found **10,097** of **155,867**

Sort results by


[Save results to a Binder](#)

 Try an [Advanced Search](#)

Display results


[Search Tips](#)

 Try this search in [The ACM Guide](#)
☐ Open results in a new window

Results 1 - 20 of 200

 Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

Best 200 shown

 Relevance scale ☐ ☐ ☐ ☐ ☐

# 1 [Approximating optimal spare capacity allocation by successive survivable routing](#)

Yu Liu, David Tipper, Peerapon Siripongwutikorn

 February 2005 **IEEE/ACM Transactions on Networking (TON)**, Volume 13 Issue 1

 Full text available: [pdf\(1.27 MB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The design of survivable mesh based communication networks has received considerable attention in recent years. One task is to route backup paths and allocate spare capacity in the network to guarantee seamless communications services survivable to a set of failure scenarios. This is a complex multi-constraint optimization problem, called the spare capacity allocation (SCA) problem. This paper unravels the SCA problem structure using a matrix-based model, and develops a fast and efficient approx ...

**Keywords:** MPLS traffic engineering, multi-commodity flow, network planning and optimization, network survivability, protection and restoration, spare capacity allocation, survivable routing

## 2 [Analog design and evaluation: Effects of noise and nonlinearity on the calibration of a non-binary capacitor array in a successive approximation analog-to-digital converter](#)

Jianhua Gan, Shouli Yan, Jacob Abraham

January 2004

 Full text available: [pdf\(113.94 KB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#)

[Publisher Site](#)

A successive approximation analog-to-digital converter using a non-binary capacitor array is presented. A perceptron learning rule is used as the capacitor calibration algorithm. The nonlinearity is analyzed using the Volterra series. The effects of noise and nonlinearity are modeled to verify the calibration robustness. With the presence of noise and nonlinearity, the capacitor weights are adaptively calibrated to match the physical capacitors with better than 22-bit accuracy. The accuracy is n ...

### 3 [VLSI design: Design and modeling of a 16-bit 1.5MSPS successive approximation ADC with non-binary capacitor array](#)

Jianhua Gan, Shouli Yan, Jacob Abraham

 April 2003 **Proceedings of the 13th ACM Great Lakes symposium on VLSI**

 Full text available: [pdf\(125.17 KB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


The design and modeling of a high performance successive approximation analog-to-digital converter (ADC) using non-binary capacitor array are presented in this paper. A non-binary capacitor array with 20 capacitors is used to design a 16-bit, 1.5 mega samples per second (MSPS) successive approximation ADC. A perceptron learning rule, originally developed for Artificial Intelligence applications, is used as the capacitor calibration algorithm. The system architecture and the circuit design for th ...

**Keywords:** analog-to-digital converter, calibration, non-binary capacitor array, successive approximation

#### 4 Using regular approximations for generalisation during partial evaluation

John P. Gallagher, Julio C. Peralta

November 1999 **ACM SIGPLAN Notices , Proceedings of the 2000 ACM SIGPLAN workshop on Partial evaluation and semantics-based program manipulation**, Volume 34 Issue 11

Full text available:  pdf(1.51 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

On-line partial evaluation algorithms include a generalisation step, which is needed to ensure termination. In partial evaluation of logic and functional programs, the usual generalisation operation applied to computation states is the most specific generalisation (*msg*) of expressions. This can cause loss of information, which is especially serious in programs whose computations first build some internal data structure, which is then used to control a subsequent phase of execution - a ...

#### 5 Lattice Approximations to the Minima of Functions of Several Variables

Gerald Berman

April 1969 **Journal of the ACM (JACM)**, Volume 16 Issue 2

Full text available:  pdf(509.50 KB)


Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A computer-oriented method is developed for determining relative minima of functions of several variables. No derivatives (or approximations) are required and the process always converges to a relative minimum no matter which initial point is used. Numerical examples using test functions suggested in the literature are included to illustrate the effectiveness of the algorithms. Modifications can easily be incorporated which permit the inclusion of constraints or integer-valued variables.

#### 6 Approximation algorithms for NP-complete problems on planar graphs

Brenda S. Baker

January 1994 **Journal of the ACM (JACM)**, Volume 41 Issue 1

Full text available:  pdf(1.98 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**Keywords:** Hamiltonian circuit, Hamiltonian path, NP-complete, approximation algorithms, approximation schemes, dominating set, independent set, partition into perfect matchings, partition into triangles, planar graphs, vertex cover

#### 7 Algorithm 458: discrete linear L1 approximation by interval linear programming

P. D. Robers, S. S. Robers

October 1973 **Communications of the ACM**, Volume 16 Issue 10

Full text available:  pdf(928.54 KB)


Additional Information: [full citation](#), [references](#)

**Keywords:** L, approximation, discrete approximation

8 Using randomized sparsification to approximate minimum cuts

David R. Karger

January 1994 **Proceedings of the fifth annual ACM-SIAM symposium on Discrete algorithms**

Full text available:  [pdf\(1.06 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

9 Parallel Multilevel Sparse Approximate Inverse Preconditioners in Large Sparse Matrix Computations

Kai Wang, Jun Zhang, Chi Shen

November 2003 **Proceedings of the 2003 ACM/IEEE conference on Supercomputing**

Full text available:  [pdf\(226.50 KB\)](#)

Additional Information: [full citation](#), [abstract](#)


We investigate the use of the multistep successive preconditioning strategies (MSP) to construct a class of parallel multilevel sparse approximate inverse (SAI) preconditioners. We do not use independent set ordering, but a diagonal dominance based matrix permutation to build a multilevel structure. The purpose of introducing multilevel structure into SAI is to enhance the robustness of SAI for solving difficult problems. Forward and backward preconditioning iteration and two Schur complement pr ...

**Keywords:** Parallel preconditioning, sparse approximate inverse, multilevel preconditioning

10 Reaching approximate agreement in the presence of faults

Danny Dolev, Nancy A. Lynch, Shlomit S. Pinter, Eugene W. Stark, William E. Weihl

May 1986 **Journal of the ACM (JACM)**, Volume 33 Issue 3

Full text available:  [pdf\(1.47 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

This paper considers a variant of the Byzantine Generals problem, in which processes start with arbitrary real values rather than Boolean values or values from some bounded range, and in which approximate, rather than exact, agreement is the desired goal. Algorithms are presented to reach approximate agreement in asynchronous, as well as synchronous systems. The asynchronous agreement algorithm is an interesting contrast to a result of Fischer et al, who show that exact agreement with guarra ...

11 Analysis methodology: On-line error bounds for steady-state approximations: a potential solution to the initialization bias problem

Enver Yücesan, Luk N. Van Wassenhove, Klenthis Papanikas, Nico M. van Dijk

December 2001 **Proceedings of the 33nd conference on Winter simulation**

Full text available:  [pdf\(274.24 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

By studying performance measures via reward structures, on-line error bounds are obtained by successive approximation. These bounds indicate when to terminate computation with guaranteed accuracy; hence, they provide insight into steady-state convergence. The method therefore presents a viable alternative to steady-state computer simulation where the output series is typically contaminated with initialization bias whose impact on the output cannot be easily quantified. The method is illustrated ...

12 Successive approximations and computer storage problems in ordinary differential equations

Richard Bellman


May 1961 **Communications of the ACM**, Volume 4 Issue 5

Full text available:  [pdf\(200.53 KB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

13 Solving minimum-cost flow problems by successive approximation

A. Goldberg, R. Tarjan

January 1987 **Proceedings of the nineteenth annual ACM conference on Theory of computing**


Full text available:  [pdf\(1.35 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We introduce a framework for solving minimum-cost flow problems. Our approach measures the quality of a solution by the amount that the complementary slackness conditions are violated. We show how to extend techniques developed for the maximum flow problem to improve the quality of a solution. This framework allows us to achieve  $O(\min(n^3, n^{5/3} m^{2/3}, \dots))$ .

14 A general framework for semantics-based bottom-up abstract interpretation of logic programs

Roberto Barbuti, Roberto Giacobazzi, Giorgio Levi

January 1993 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 15 Issue 1

Full text available:  [pdf\(2.99 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

The theory of abstract interpretation provides a formal framework to develop advanced dataflow analysis tools. The idea is to define a nonstandard semantics which is able to compute, in finite time, an approximated model of the program. In this paper, we define an abstract interpretation framework based on a fixpoint approach to the semantics. This leads to the definition, by means of a suitable set of operators, of an abstract fixpoint characterization of a model associated with the program ...

**Keywords:** abstract interpretation, logic programming, program analysis

15 Best approximate circles on integer grids

M. D. McIlroy

October 1983 **ACM Transactions on Graphics (TOG)**, Volume 2 Issue 4

Full text available:  [pdf\(1.34 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)

16 Session 11B: Efficient sequences of trials

Edith Cohen, Amos Fiat, Haim Kaplan


January 2003 **Proceedings of the fourteenth annual ACM-SIAM symposium on Discrete algorithms**

Full text available:  [pdf\(873.28 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We introduce a problem called *sequential trial optimization*, a generalization of the well studied set cover problem with a new objective function. We give a simple algorithm that achieves a constant factor approximation to this problem. Sequential trial optimization naturally arises in heterogeneous search environments such as peer to peer networks.

**17 Suspension analyses for concurrent logic programs**


Michael Codish, Moreno Falaschi, Kim Marriott

May 1994 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,  
Volume 16 Issue 3Full text available:  [pdf\(2.26 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Concurrent logic languages specify reactive systems which consist of collections of communicating processes. The presence of unintended suspended computations is a common programming error which is difficult to detect using standard debugging and testing techniques. We develop a number of analyses, based on abstract interpretation, which succeed if a program is definitely suspension free. If an analysis fails, the program may, or may not, be suspension free. Examples demonstrate that the an ...

**Keywords:** abstract interpretation, concurrent logic programming, program analysis**18 Data structures for quadtree approximation and compression**

Hanan Samet

September 1985 **Communications of the ACM**, Volume 28 Issue 9Full text available:  [pdf\(1.86 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

A number of data structures for representing images by quadtrees without pointers are discussed. The image is treated as a collection of leaf nodes. Each leaf node is represented by use of a locational code corresponding to a sequence of directional codes that locate the leaf along a path from the root of the tree. Somewhat related is the concept of a forest which is a representation that consists of a collection of maximal blocks. It is reviewed and refined to enable the representation of ...

**19 An analysis of oblivious and adaptive routing in optical networks with wavelength translation**

Jonathan P. Lang, Vishal Sharma, Emmanouel A. Varvarigos

August 2001 **IEEE/ACM Transactions on Networking (TON)**, Volume 9 Issue 4Full text available:  [pdf\(377.54 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We present an analysis for both oblivious and adaptive routing in regular, all-optical networks with wavelength translation. Our approach is simple, computationally inexpensive, accurate for both low and high network loads, and the first to analyze adaptive routing with wavelength translation in wavelength division multiplexed (WDM) networks while also providing a simpler formulation of oblivious routing with wavelength translation. Unlike some previous analyses which use the link independence b ...

**Keywords:** Adaptive routing, all-optical networks, hypercube, multi-fiber networks, oblivious routing, performance analysis, torus, wavelength division multiplexing, wavelength translation**20 Improved approximation algorithms for the multi-commodity flow problem and local competitive routing in dynamic networks**

Baruch Awerbuch, Tom Leighton

May 1994 **Proceedings of the twenty-sixth annual ACM symposium on Theory of computing**Full text available:  [pdf\(909.75 KB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Results 1 - 20 of 200

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2005 ACM, Inc.

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

**Search Results**[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "( successive &lt;in&gt;metadata ) &lt;and&gt; ( approximation&lt;in&gt;metadata )"

☒ e-mail

Your search matched 662 of 1164322 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.[» View Session History](#)[» New Search](#)

Modify Search

( successive &lt;in&gt;metadata ) &lt;and&gt; ( approximation&lt;in&gt;metadata )

[» Key](#)

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

Select Article Information

View: 1-25 | [26-5](#)

- |                          |   |
|--------------------------|---|
| <input type="checkbox"/> | <b>1. Adaptive successive approximation quantization of image waveforms with efficie updates</b><br>Barnes, C.F.;<br>Image Processing, 1994. Proceedings. ICIP-94., IEEE International Conference<br>Volume 3, 13-16 Nov. 1994 Page(s):876 - 880 vol.3<br><a href="#">AbstractPlus</a>   Full Text: <a href="#">PDF(424 KB)</a> IEEE CNF                        |
| <input type="checkbox"/> | <b>2. Parallelization model for successive approximations to the Rayleigh-Ritz linear v problem</b><br>Greer, J.C.;<br>Parallel and Distributed Systems, IEEE Transactions on<br>Volume 9, Issue 10, Oct. 1998 Page(s):938 - 946<br><a href="#">AbstractPlus</a>   <a href="#">References</a>   Full Text: <a href="#">PDF(432 KB)</a> IEEE JNL                 |
| <input type="checkbox"/> | <b>3. Convergent algorithms for successive approximation vector quantisation with a wavelet image compression</b><br>Craizer, M.; da Silva, E.A.B.; Ramos, E.G.;<br>Vision, Image and Signal Processing, IEE Proceedings-<br>Volume 146, Issue 3, June 1999 Page(s):159 - 164<br><a href="#">AbstractPlus</a>   Full Text: <a href="#">PDF(364 KB)</a> IEEE JNL |
| <input type="checkbox"/> | <b>4. CMOS active pixel sensor with on-chip successive approximation analog-to-digit</b><br>Zhimin Zhou; Pain, B.; Fossum, E.R.;<br>Electron Devices, IEEE Transactions on<br>Volume 44, Issue 10, Oct. 1997 Page(s):1759 - 1763<br><a href="#">AbstractPlus</a>   <a href="#">References</a>   Full Text: <a href="#">PDF(124 KB)</a> IEEE JNL                 |
| <input type="checkbox"/> | <b>5. An enhanced successive-approximation circuit for A/D conversion</b><br>Webster, C.M.; Comer, D.T.;<br>Custom Integrated Circuits Conference, 1993., Proceedings of the IEEE 1993<br>9-12 May 1993 Page(s):28.2.1 - 28.2.4<br><a href="#">AbstractPlus</a>   Full Text: <a href="#">PDF(180 KB)</a> IEEE CNF   |
| <input type="checkbox"/> | <b>6. Successive approximation solution of the HJI equation</b><br>Wise, K.A.; Sedwick, J.L.;<br>Decision and Control, 1994., Proceedings of the 33rd IEEE Conference on<br>Volume 2, 14-16 Dec. 1994 Page(s):1387 - 1391 vol.2   |

[AbstractPlus](#) | Full Text: [PDF\(276 KB\)](#) IEEE CNF

- ☐ 7. **Image coding using successive approximation wavelet vector quantization**  
da Silva, E.A.B.; Sampson, D.G.; Ghanbari, M.;  
Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., 1995 International Conf  
Volume 4, 9-12 May 1995 Page(s):2201 - 2204 vol.4  
[AbstractPlus](#) | Full Text: [PDF\(380 KB\)](#) IEEE CNF
- ☐ 8. **Image compression using zerotree and multistage vector quantization**  
Karlekar, J.; Poonacha, P.G.; Desai, U.B.;  
Image Processing, 1997. Proceedings., International Conference on  
Volume 2, 26-29 Oct. 1997 Page(s):610 - 612 vol.2  
[AbstractPlus](#) | Full Text: [PDF\(424 KB\)](#) IEEE CNF
- ☐ 9. **A vertical successive-approximation A/D converter architecture for high-speed a**  
Hamdy, N.; Soliman, H.; Eid, A.;  
Circuits and Systems, 1998. Proceedings. 1998 Midwest Symposium on  
9-12 Aug. 1998 Page(s):542 - 545  
[AbstractPlus](#) | Full Text: [PDF\(328 KB\)](#) IEEE CNF
- ☐ 10. **A successive approximation vector quantizer for wavelet transform image coding**  
Da Silva, E.A.B.; Sampson, D.G.; Ghanbari, M.;  
Image Processing, IEEE Transactions on  
Volume 5, Issue 2, Feb. 1996 Page(s):299 - 310  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1872 KB\)](#) IEEE JNL
- ☐ 11. **A 10-bit 5-MS/s successive approximation ADC cell used in a 70-MS/s ADC array**  
Jiren Yuan; Svensson, C.;  
Solid-State Circuits, IEEE Journal of  
Volume 29, Issue 8, Aug. 1994 Page(s):866 - 872  
[AbstractPlus](#) | Full Text: [PDF\(840 KB\)](#) IEEE JNL
- ☐ 12. **A successive double-bit approximation technique for analog/digital conversion**  
Bhandari, S.M.; Aggarwal, S.;  
Circuits and Systems, IEEE Transactions on  
Volume 37, Issue 6, June 1990 Page(s):856 - 858  
[AbstractPlus](#) | Full Text: [PDF\(256 KB\)](#) IEEE JNL
- ☐ 13. **A high-resolution synchronous mirror delay using successive approximation reg**  
Kihyuk Sung; Lee-Sup Kim;  
Solid-State Circuits, IEEE Journal of  
Volume 39, Issue 11, Nov. 2004 Page(s):1997 - 2004  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(768 KB\)](#) IEEE JNL
- ☐ 14. **Deliberation with "fast full-search block matching"**  
Junming Lu; Zhenghui Lin;  
Circuits and Systems for Video Technology, IEEE Transactions on  
Volume 13, Issue 1, Jan. 2003 Page(s):97 - 99  
[AbstractPlus](#) | Full Text: [PDF\(278 KB\)](#) IEEE JNL
- ☐ 15. **Successive order scattering transport approximation for laser light propagation i**  
**medium**  
Jungkuk Kim; Lin, J.C.;  
Biomedical Engineering, IEEE Transactions on  
Volume 45, Issue 4, April 1998 Page(s):505 - 510  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(196 KB\)](#) IEEE JNL



- ☐ **16. Successive variational approximations of impedance parameters in a coupled an**  
Hu, M.; Hu, Y.;  
Antennas and Propagation, IEEE Transactions on [legacy, pre - 1988]  
Volume 7, Issue 4, Oct 1959 Page(s):373 - 379  
[AbstractPlus](#) | Full Text: [PDF\(640 KB\)](#) IEEE JNL
  
- ☐ **17. A CMOS implementation of a video-rate successive approximation A/D converter**  
Chen, K.; Svensson, C.; Yuan, J.-R.;  
Circuits and Systems, 1988., IEEE International Symposium on  
7-9 June 1988 Page(s):2577 - 2580 vol.3  
[AbstractPlus](#) | Full Text: [PDF\(268 KB\)](#) IEEE CNF
  
- ☐ **18. Fast successive-approximation A/D converters**  
Hadidi, K.; Tso, V.S.; Temes, G.C.;  
Custom Integrated Circuits Conference, 1990., Proceedings of the IEEE 1990  
13-16 May 1990 Page(s):6.1/1 - 6.1/4  
[AbstractPlus](#) | Full Text: [PDF\(276 KB\)](#) IEEE CNF
  
- ☐ **19. ALSAC, an automatic layout system for successive approximation converters**  
Yufera, A.; Rueda, A.; Huertas, J.L.; Paris, L.; Osés, T.;  
Circuits and Systems, 1991. Conference Proceedings, China., 1991 International Conf  
16-17 June 1991 Page(s):407 - 410 vol.1  
[AbstractPlus](#) | Full Text: [PDF\(444 KB\)](#) IEEE CNF
  
- ☐ **20. A switched-capacitor successive-approximation analog-to-digital converter**  
Ogawa, S.; Watanabe, K.;  
Circuits and Systems, 1991., IEEE International Symoisum on  
11-14 June 1991 Page(s):1577 - 1580 vol.3  
[AbstractPlus](#) | Full Text: [PDF\(240 KB\)](#) IEEE CNF
  
- ☐ **21. Successive approximation radial basis function networks for nonlinear modeling**  
Xiangdong He; Lapedes, A.;  
Neural Networks, 1993. IJCNN '93-Nagoya. Proceedings of 1993 International Joint Co  
Volume 2, 25-29 Oct. 1993 Page(s):1997 - 2000 vol.2  
[AbstractPlus](#) | Full Text: [PDF\(400 KB\)](#) IEEE CNF
  
- ☐ **22. Successive approximation quantization with generalized decoding for wavelet tr:  
coding**  
Barnes, C.F.; Holder, E.J.;  
Signals, Systems and Computers, 1993. 1993 Conference Record of The Twenty-Seve  
Conference on  
1-3 Nov. 1993 Page(s):533 - 537 vol.1  
[AbstractPlus](#) | Full Text: [PDF\(428 KB\)](#) IEEE CNF
  
- ☐ **23. Continuous-time leapfrog filter with precise successive approximation tuning**  
Nunes, F.C.; Franca, J.E.;  
Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on  
3-6 May 1993 Page(s):1271 - 1273 vol.2  
[AbstractPlus](#) | Full Text: [PDF\(324 KB\)](#) IEEE CNF
  
- ☐ **24. Segment-oriented coding of textured images based on successive approximation**  
Kaup, A.; Aach, T.;  
Speech, Image Processing and Neural Networks, 1994. Proceedings, ISSIPNN '94., 19  
Symposium on  
13-16 April 1994 Page(s):197 - 200 vol.1  
[AbstractPlus](#) | Full Text: [PDF\(336 KB\)](#) IEEE CNF

**25. A forward successive approximation A/D converter architecture**

Hamdy, N.; Soliman, H.;  
Circuits and Systems, 1996., IEEE 39th Midwest symposium on  
Volume 1, 18-21 Aug. 1996 Page(s):234 - 237 vol.1

[AbstractPlus](#) | Full Text: [PDF](#)(232 KB) IEEE CNF



View: **1-25** | [26-5](#)

[Help](#) [Contact Us](#) [Privacy & :](#)

© Copyright 2005 IEEE --

Indexed by  
 Inspec®

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	0	successive approximation analog digital sample input signal digital code logic intermediate value comparator reference voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:38
L5	1	successive approximation analog digital sample input signal digital code logic comparator reference voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/05/27 13:39
L8	1	successive approximation analog digital sample input signal digital code logic comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/05/27 13:39
L9	1	successive approximation analog digital input signal digital code logic comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/05/27 13:39
L12	4	successive approximation analog digital input signal digital code comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/05/27 13:40
L4	5	successive approximation analog digital sample input signal digital code logic comparator reference voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:38
L6	5	successive approximation analog digital sample input signal digital code logic comparator reference voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:39
L7	5	successive approximation analog digital sample input signal digital code logic comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:39

L10	20	successive approximation analog digital input signal digital code logic comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:39
L13	57	successive approximation analog digital input signal digital comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/05/27 13:40
L17	57	successive\$ approximation analog digital input signal digital comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/05/27 13:41
L11	60	successive approximation analog digital input signal digital code comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:40
L18	60	successive\$ approximat\$ analog digital input signal digital comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/05/27 13:41
L19	114	successive\$ approximat\$ analog digital input signal digital voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/05/27 13:41
L15	418	successive approximation analog digital input signal digital compara\$tor voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:40
L14	434	successive approximation analog digital input signal digital comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:40

L16	437	successive\$ approximation analog digital input signal digital comparator voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/05/27 13:41
L2	613	successive approximation analog digital sample input signal digital code logic intermediate value comparator reference voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/05/27 13:38
L1	2390	((341/156,155,158,161,162, 163141) or (307/240,241)).CCLS.	USPAT	OR	OFF	2005/05/27 13:31